

# CS 315-02 Lab Processor Design JAL JALR

## Lab 06 Part 1

addi (li)

add

unimp

## Lab 06 Part 1

jal

jalr

(call)

(ret)

jal jump and link J-type

update ra (rd) to PC+4

update PC to PC+imm

jalr jump to register value and link

(ret)

update rd to PC+4

i-type

update PC to rs1 + imm

↑  
ra

↑  
0

